



Avalent Technologies, Inc.

Ultimate Choice for Low Power & High Performance Embedded Microprocessor

Avalent Technologies, Inc. is a fabless semiconductor company focused on developing advanced microprocessor cores for embedded wireless and telecommunications applications, including network processors using our own processor cores. Our goal is to provide our customers with high-performance, power-efficient, low-cost processor solutions for their designs.



Products

32-Bit RISC Embedded Processor Cores and Peripheral IP

The Janus & Tiber processor family

- **Janus 2cc - low power embedded processor core**

 - Janus200 - to 200 MHz

 - Janus 220 - super low power, to 250 MHz

- **Tiber - low power embedded processor core**

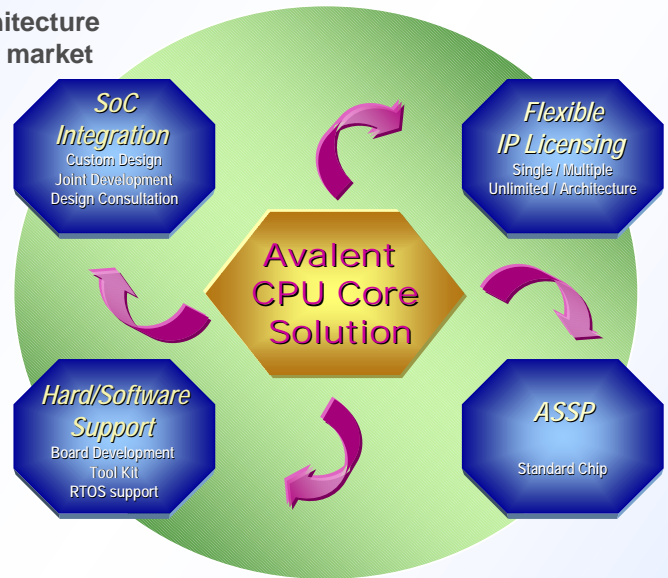
 - Tiber 1 - 250 to 300 MHz, enhanced architecture

 - Tiber 2 - to 400/500 MHz, for high-end IA market

Other IP's:

Peripheral IP's for

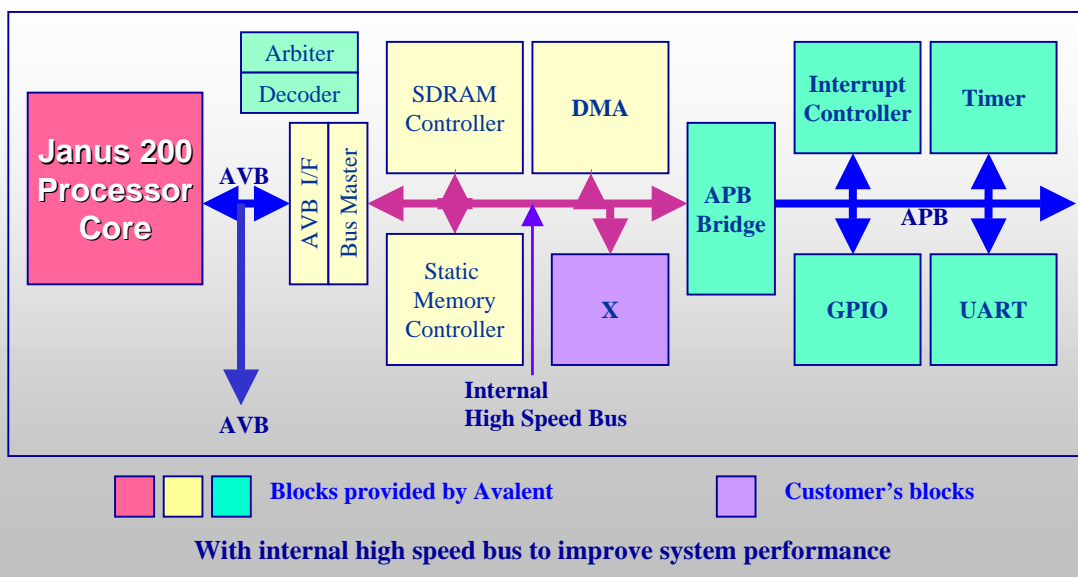
- AHB and APB interface
- Wireless – Bluetooth
- Wireless - 802.11a/b/g
- Networking – 10/100 MAC
- Security processor
- IO – USB, PCI
- Memory card I/F



System on Chips

Customer Solutions Built With Avalent IP Cores

Ultimately, at Avalent we want our customers to be satisfied with our IP. Whether they decide to build **printers, network processors, Bluetooth systems, or PDA's**, we pledge to help our customers achieve their SoC design goals and meet their fast-paced time-to-market schedules.



Training and Support

Avalent's Pledge to Customer Satisfaction

On-site training sessions for both software and hardware can be scheduled at our customers' business centers.

or

Avalent can also host your engineering training sessions in-house.



Software-

- Avasoft10: Introduction to Evaluation Board
- Avasoft15: Introduction to Embedde ICE
- Avasoft20: Introduction to Avalent's SDK
- Avasoft25: One on one training

Hardware-

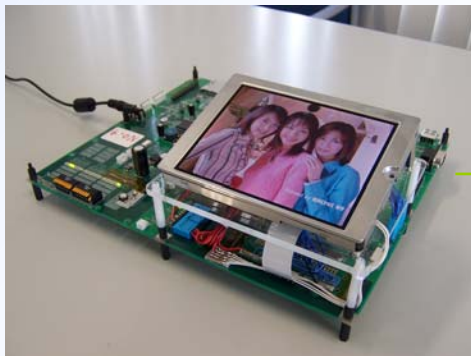
- Avasoft10: Introduction to Avalent's core
- Avasoft15: Pins and Timing
- Avasoft20: Putting it ALL Together
- Avasoft25: One on one training

Success Story

SOC and Turnkey Projects

1. Janus Wireless SOC EV board
3. Printer Controller Chip
5. Bluetooth Printer Dongle
7. Bluetooth BB Controller Chip,.....

2. Digital Image Processor
4. Network Processor Chip
6. Bluetooth Mouse



- Printer Controller Chip
- Digital Image Processor

- Network processor Chip



- Bluetooth printer Dongle



- Bluetooth Mouse



About Avalent



Introduction

Avalent's founding team of experienced SGI and Intel chip designers had a vision of creating a low-power, high-performance 32-bit RISC processor to fill a niche in the embedded microprocessor market. Starting in 1999, Avalent's engineers have upheld this vision in every milestone that we've achieved.

The net result over the last three years have been two family series of microprocessors: Tiber and Janus. Our first processor, Janus 2CC, was a cacheless 32-bit RISC processor which formed the cornerstone of the Janus Family. Soon after, Janus 200 and 220 addressed the needs of our customers adding cache and MMU functionality.

With each chipset, our vision of providing flexible and superior IP to our customers has slowly become a reality. Ultimately entering 2002, our engineering team has created the Tiber 1CC processor which forms the backbone of our new Tiber Family.

History

- ◆ 1999- company founded
- ◆ 2000- developed Janus CC, that is now the cornerstone of the Janus microprocessor family
- ◆ 2001- the Janus 200 chipset series was born
- ◆ 2002- Tiber family series processors and with both a cache and MMU and cache system
- ◆ 2003 – SOC platform
- ◆ 2004 – Bluetooth in MP

Contact Information

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